

Studying Shunted SQUID Measurements in a Controlled Magnetic Field Setting **J. Adamczyk¹**, R. Ganguly², H. Courtois², C. Winkelmann² ¹Department of Physics, Cleveland State University; ²Department QUEST, Néel Institute

In recent years, the study of nanodevices such as superconducting quantum interference devices (SQUIDs) has increased in popularity due to their usage in magnetometry, for example of the magnetism of nanoparticles. Particularly, SQUIDs have the capability of measuring small changes in magnetic field and changes in magnetization at the level of a few Bohr magnetization at the level of a few Bohr magnetons. Electrical measurements of a SQUID, which is shunted with an on-chip Au resistor, are shown in the normal and superconducting states, at temperatures down to 4 K. Stable critical currents and hysteretic voltage-current characteristics are observed. The SQUID. Data and corresponding theory showing the dependence on the device's critical current is presented as well as a discussion of these results.

Motivation

An example of a superconducting nano-device is a SQUID^{[1][2]} (Fig. 1), Super-conducting QUantum Interference Device.



Figure 1: SQUID loop fabricated at Néel Institute imaged under SEM. Area of loop calculated to be 0.983 μm^2

The device uses <u>Josephson Junctions^[5]</u> to create detectable interference patterns in electric current.

Applying magnetic flux through a SQUID loop (Fig. 1) induces a current, so we can see the effect of B-field on electronic samples.

Preparation

Our SQUID has the following specs:

- Si substrate
- 20nm Nb deposition
- < 1200Ω normal mode resistance
- *T_c* ~7K
- Loop area ~ $1\mu m^2$
- Gold shunting
- This stops electrostatic discharge from killing the device
- Critical current near 0.5mA

Dipstick now harnesses a solenoid to supply B-field to sample.

- 1. Sample is loaded into the stage
- 2. Cap is connected and sealed
- 3. Vacuum is pumped within, filled with He exchange gas
- 4. Sample connected electronically and grounded
- 5. Dipstick immersed in a liquid He dewar



Figure 2: Schematic of the configured dipstick

Abstract



sample

on critical current. Normalized by $I_{c1} - I_{c2} = 0$

electronics





